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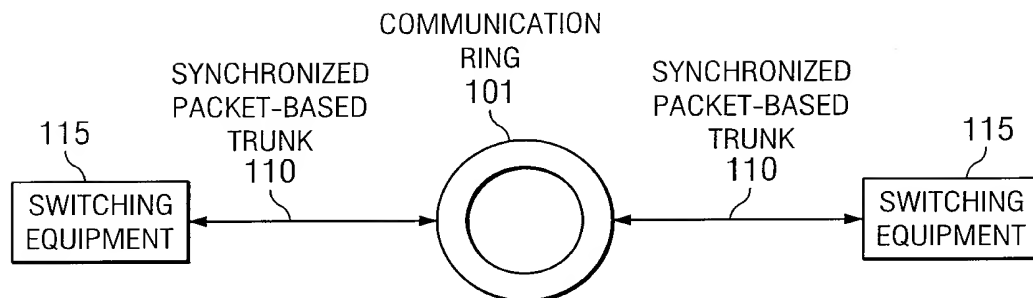


FIG. 1

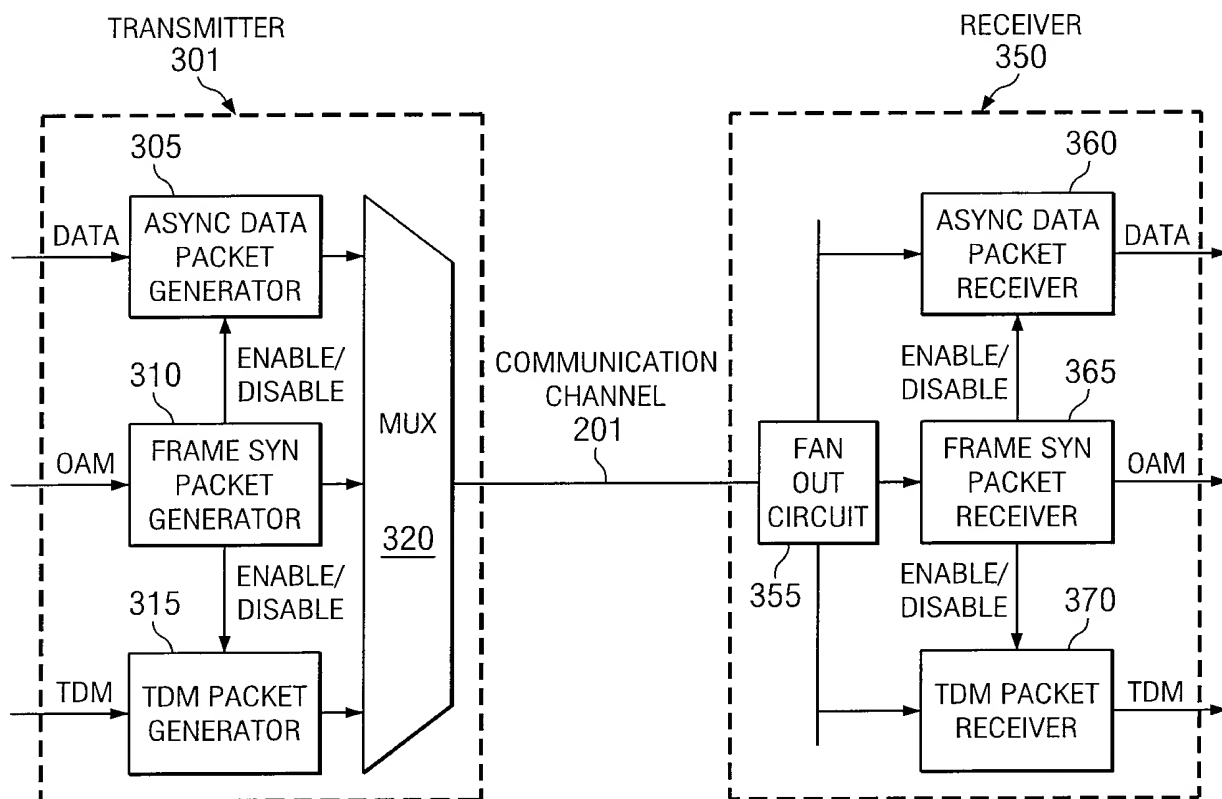


FIG. 3

FIG. 2

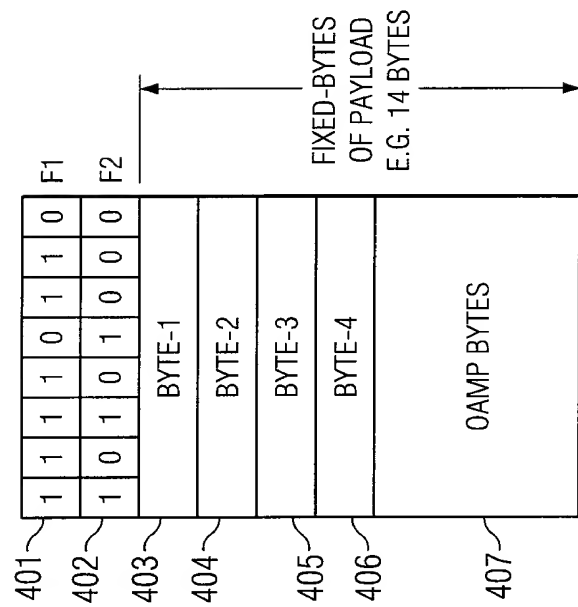
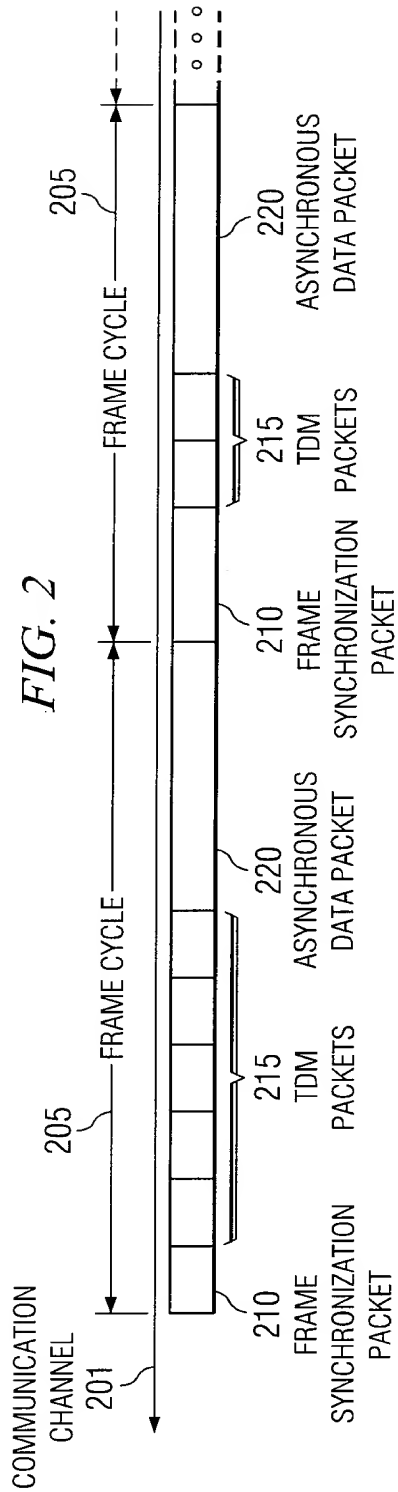


FIG. 5

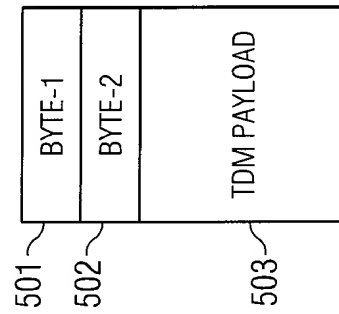
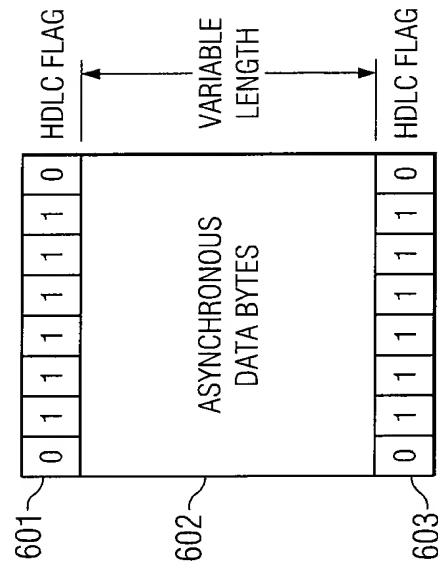
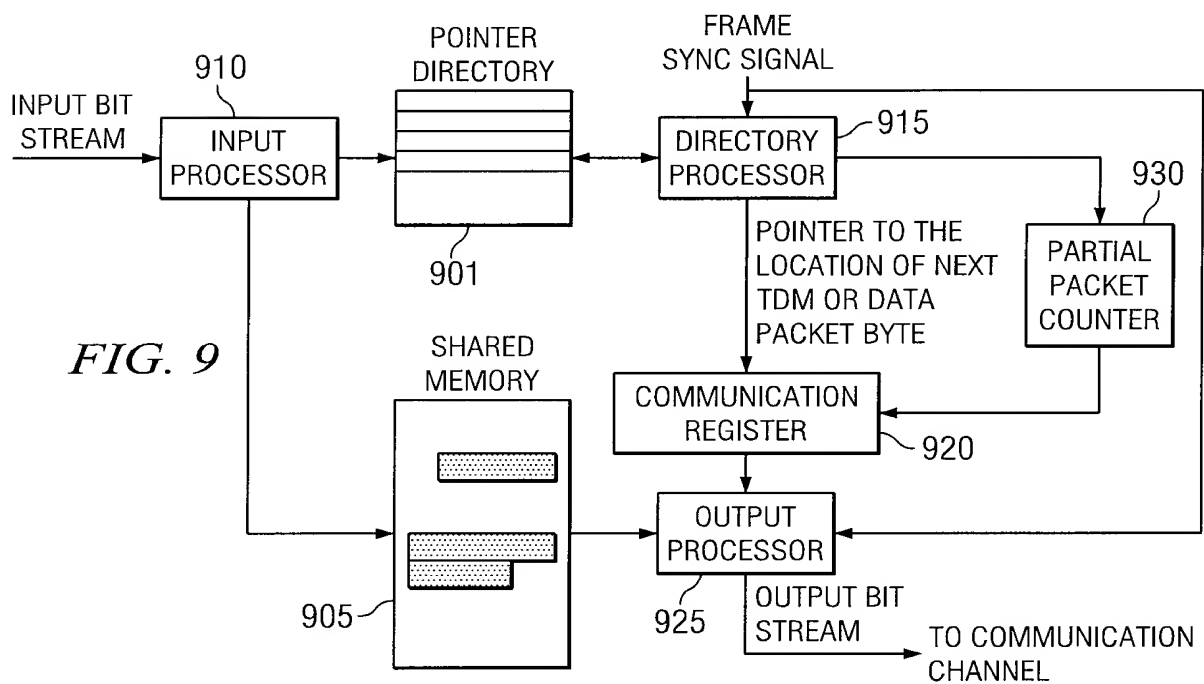
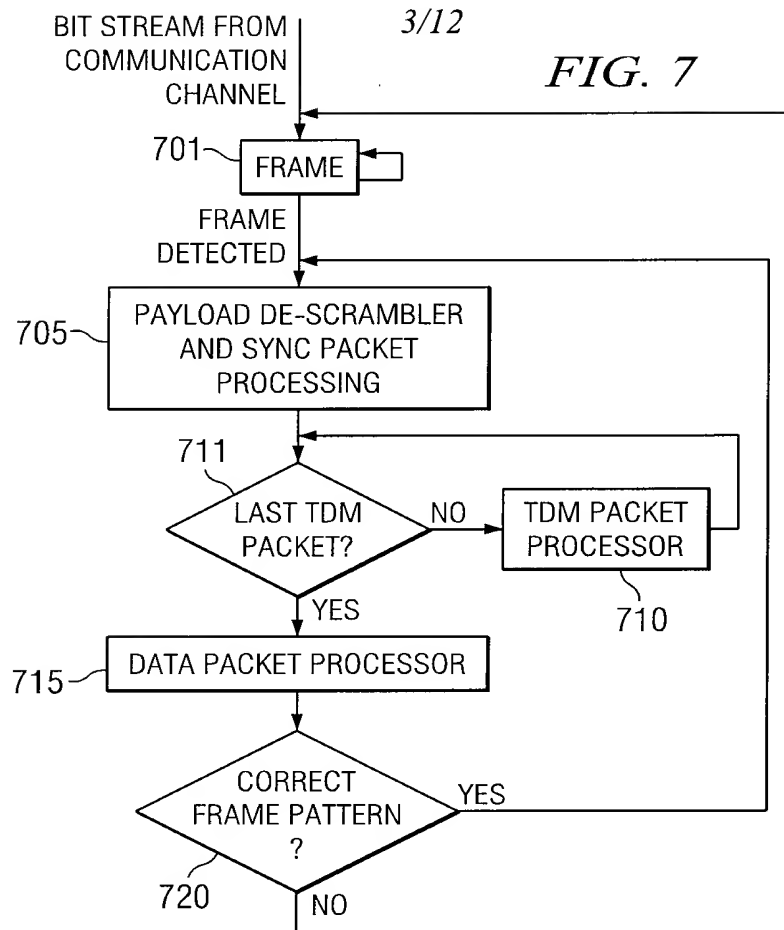


FIG. 6



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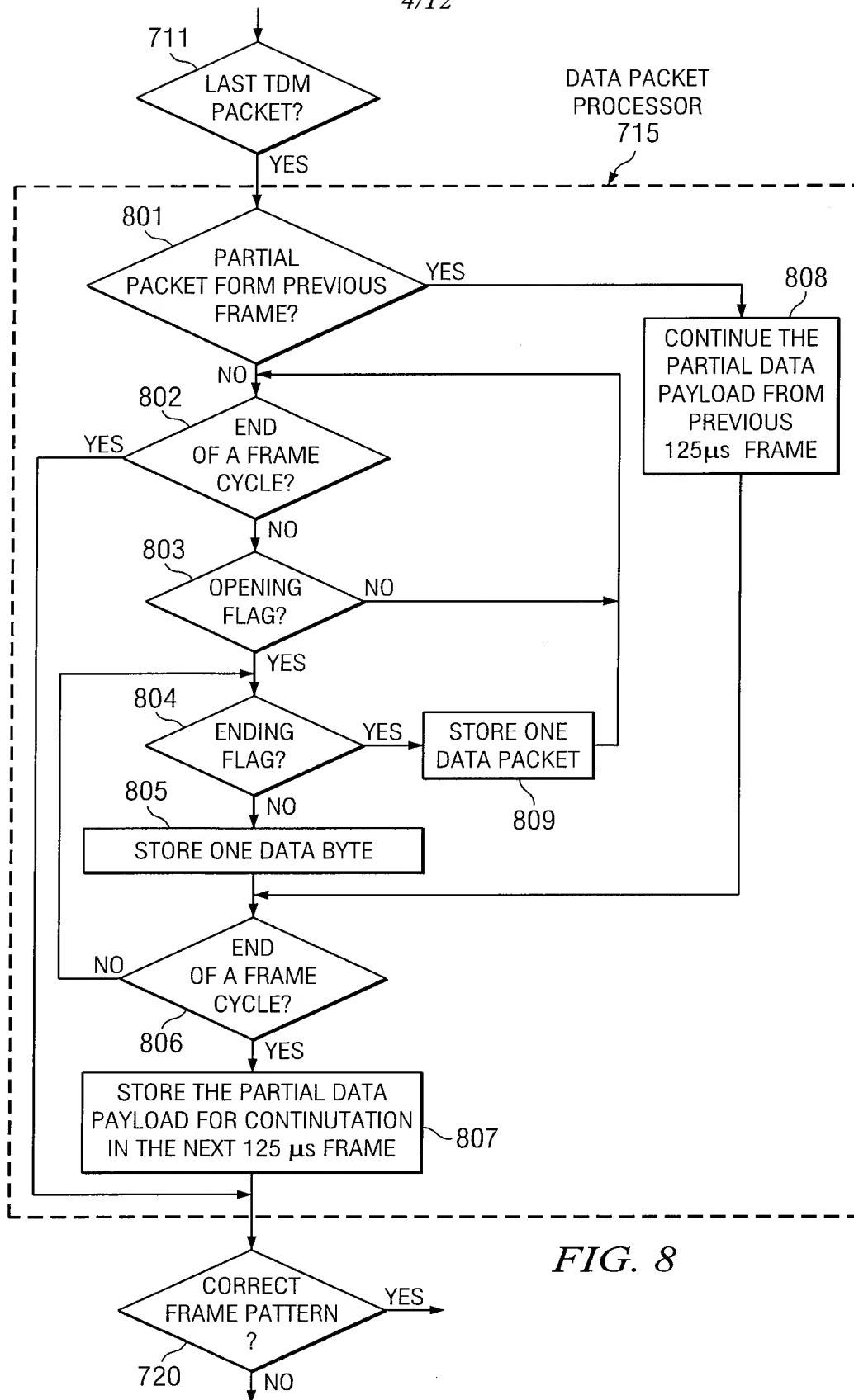


FIG. 8

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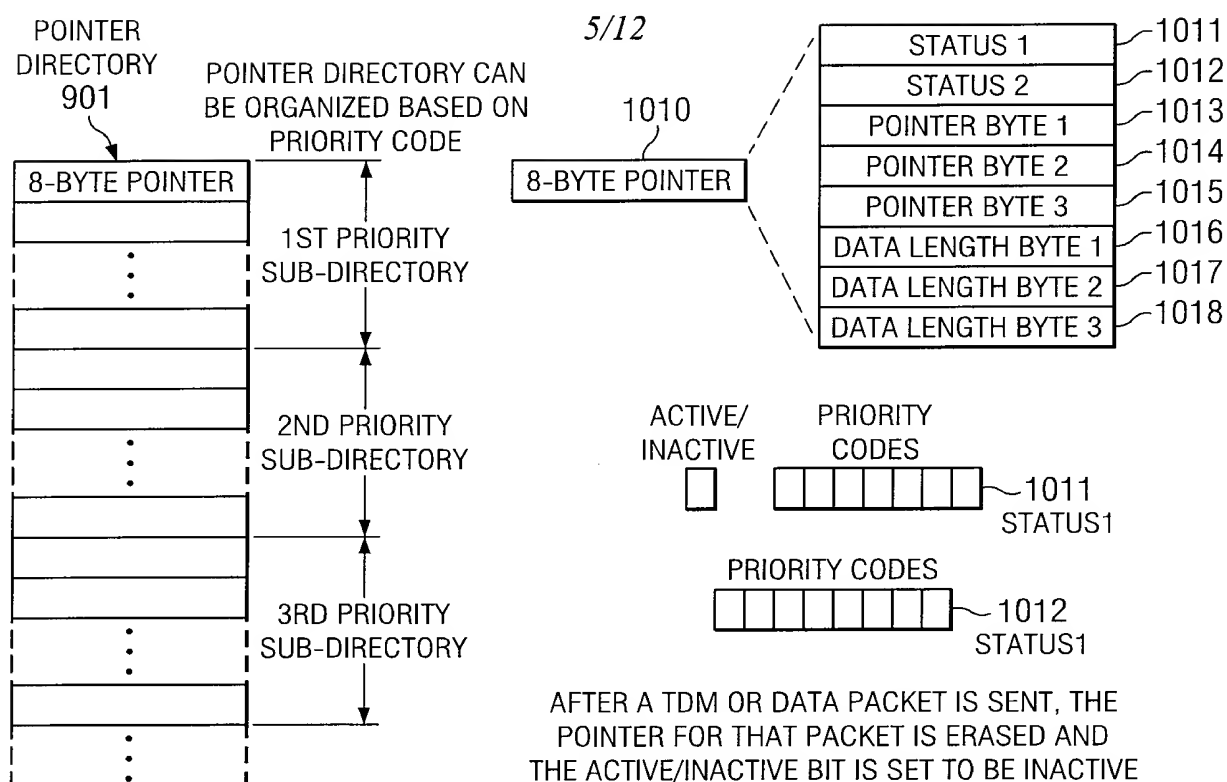
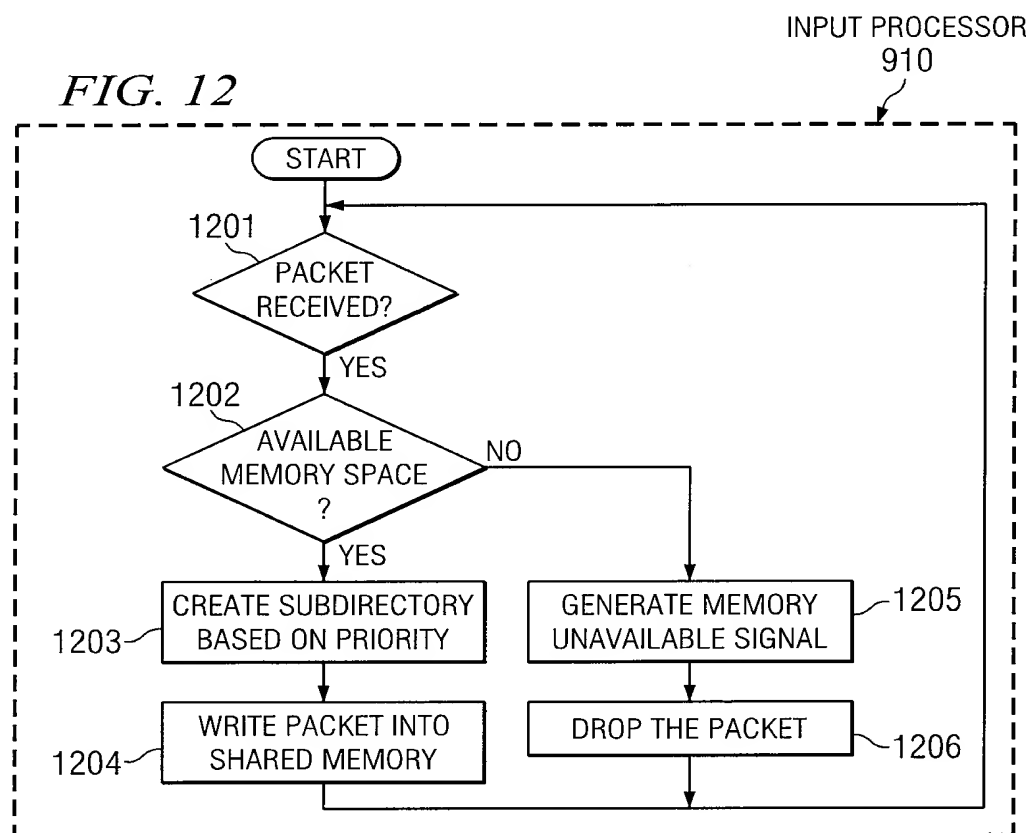
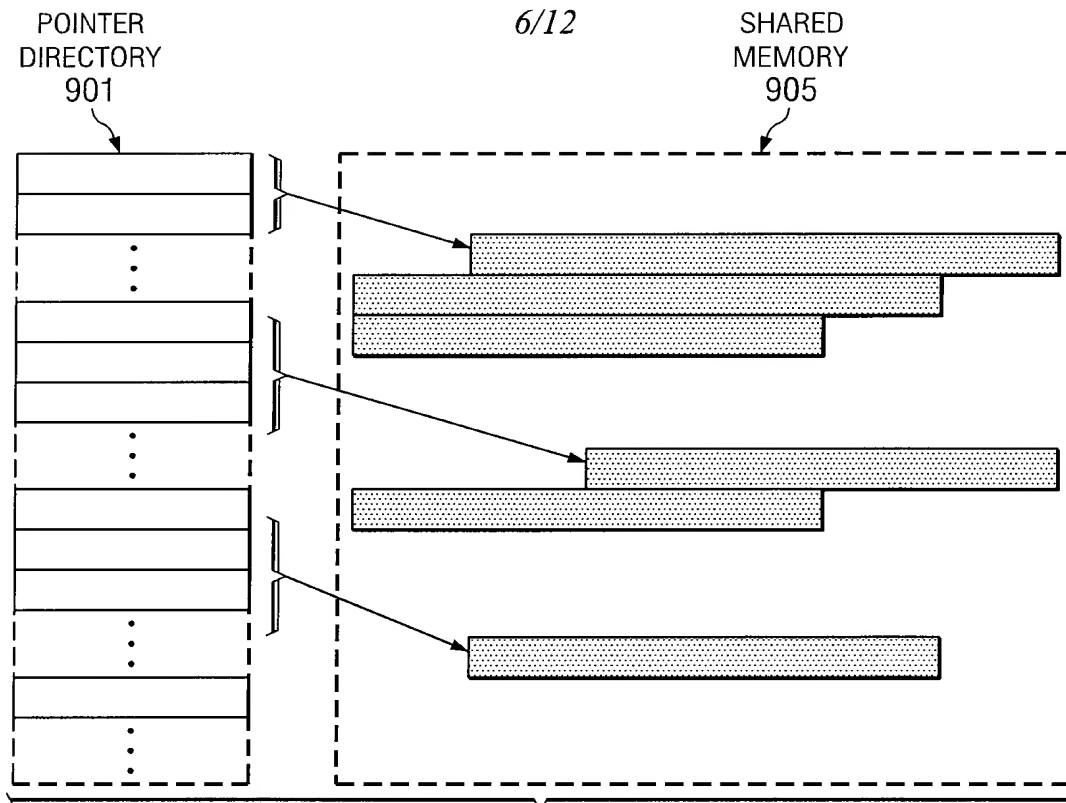
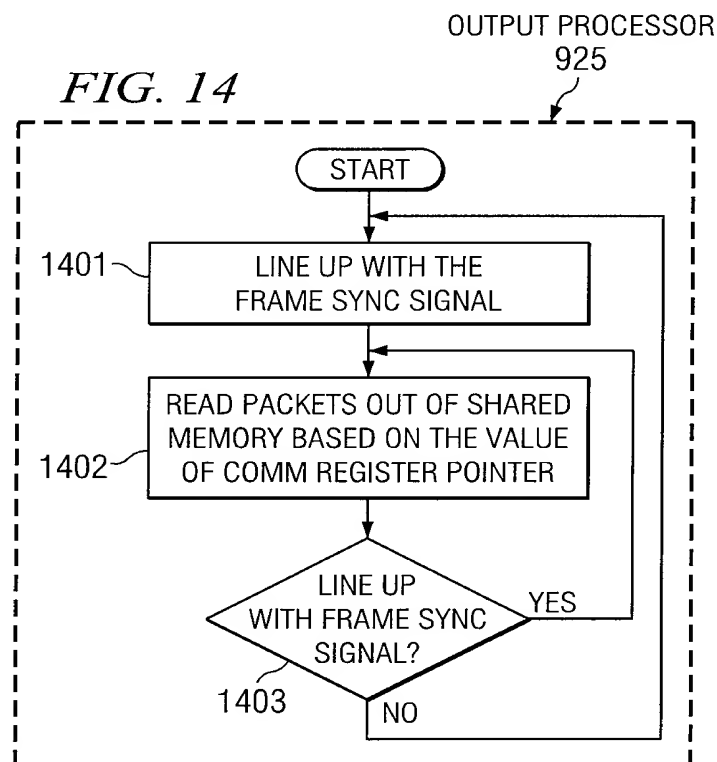


FIG. 10



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*FIG. 11*

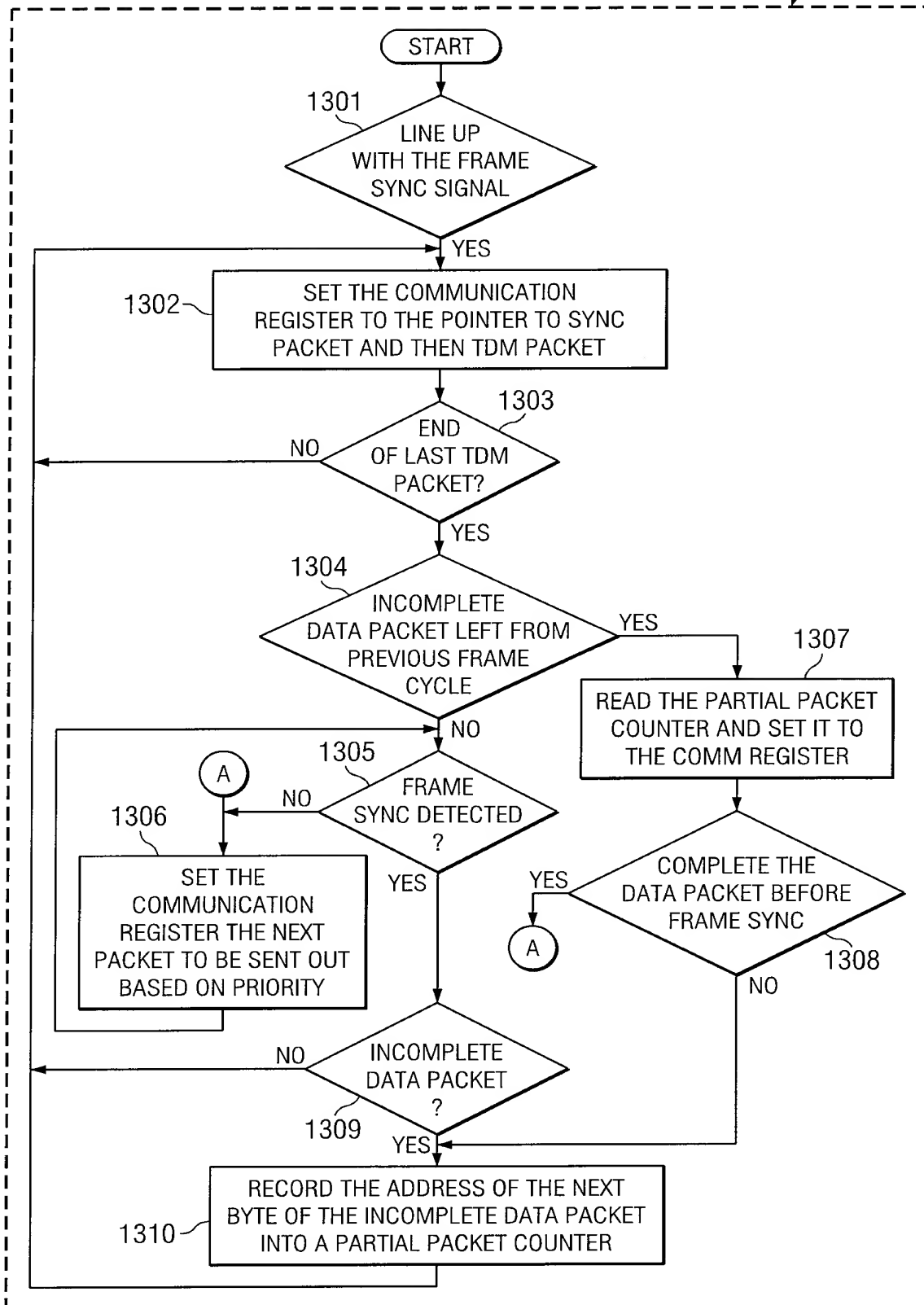
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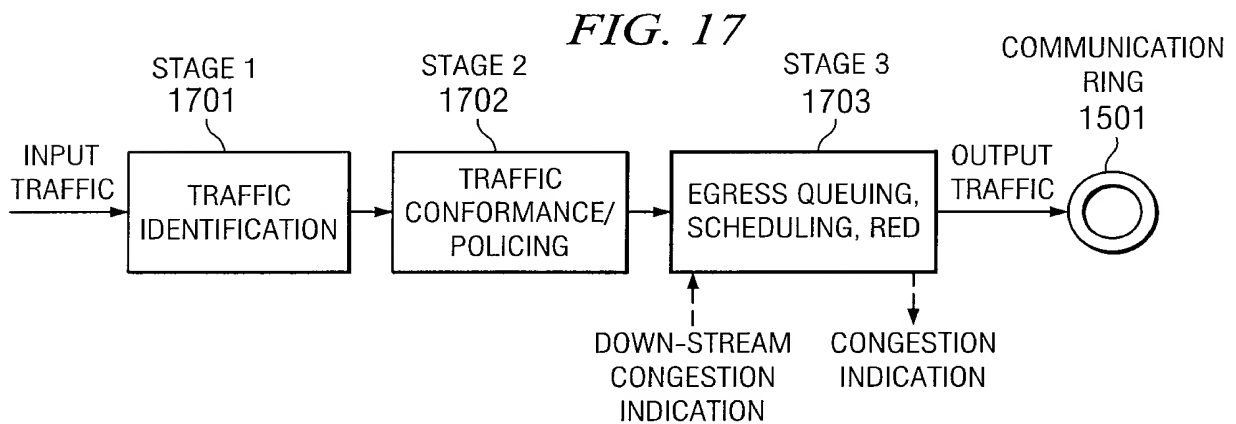
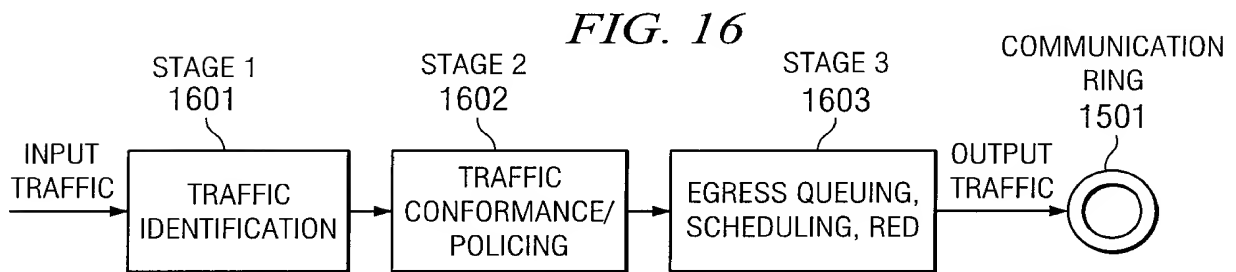
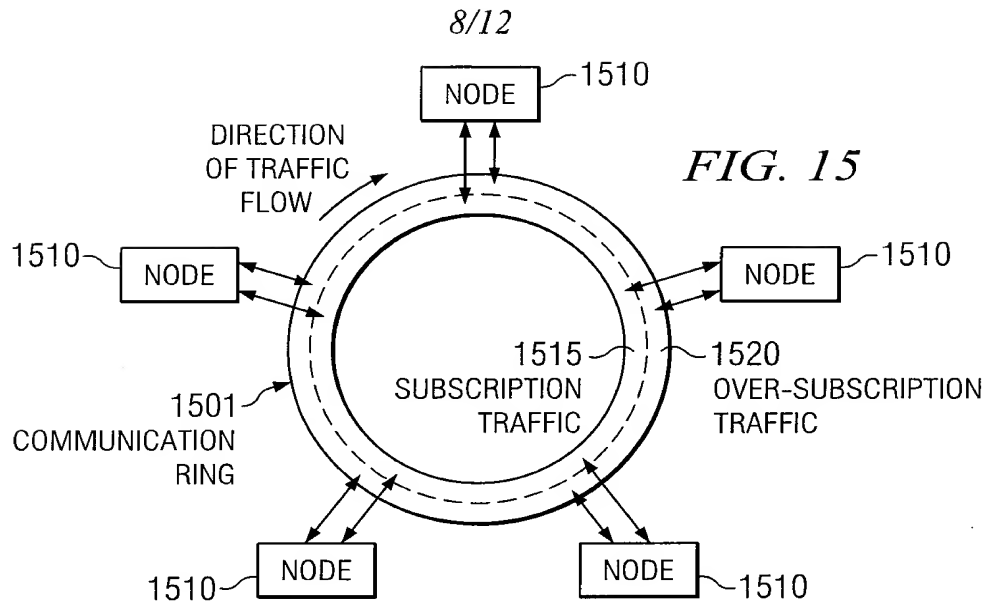
DIRECTORY PROCESSOR

915

FIG. 13



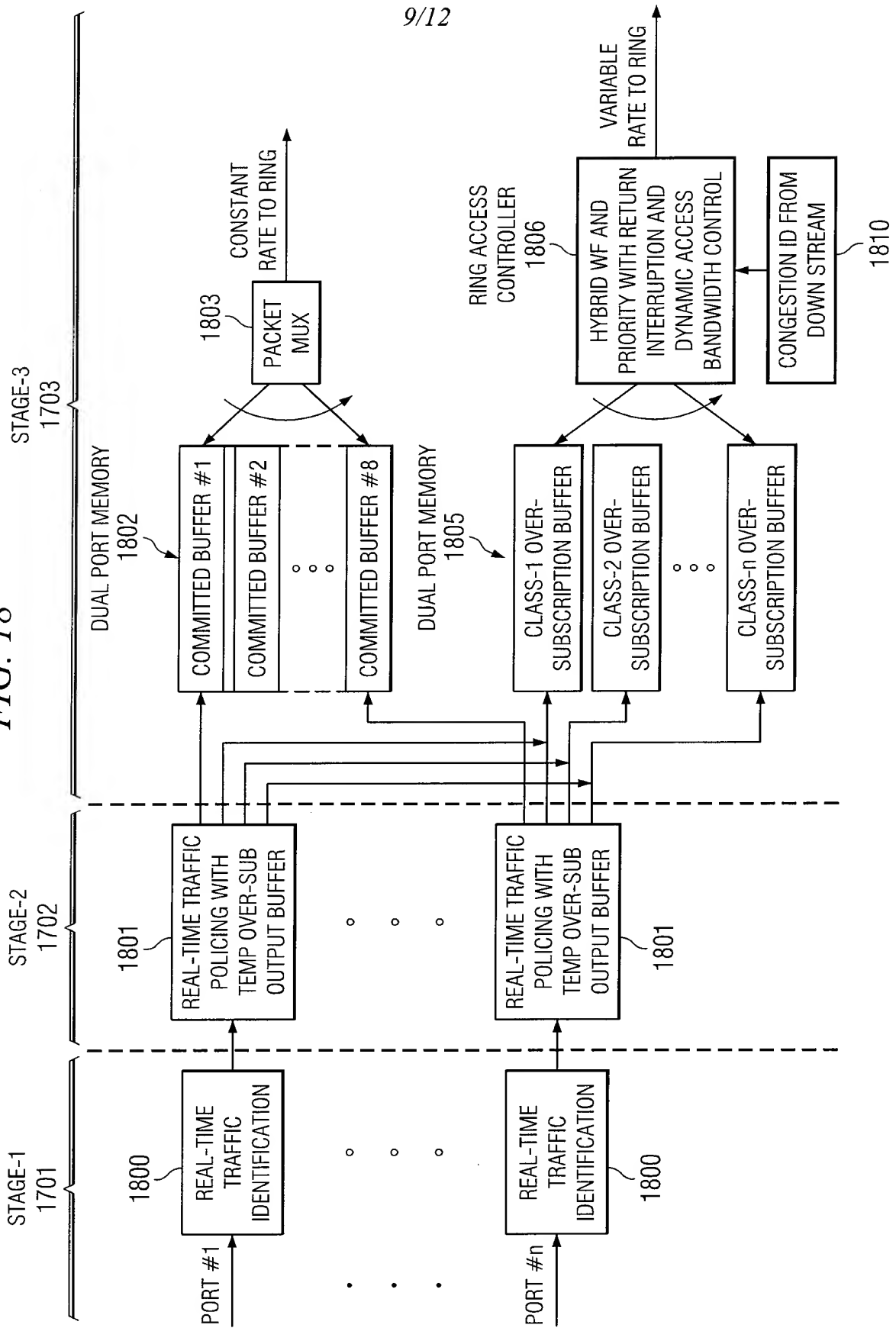
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FIG. 18



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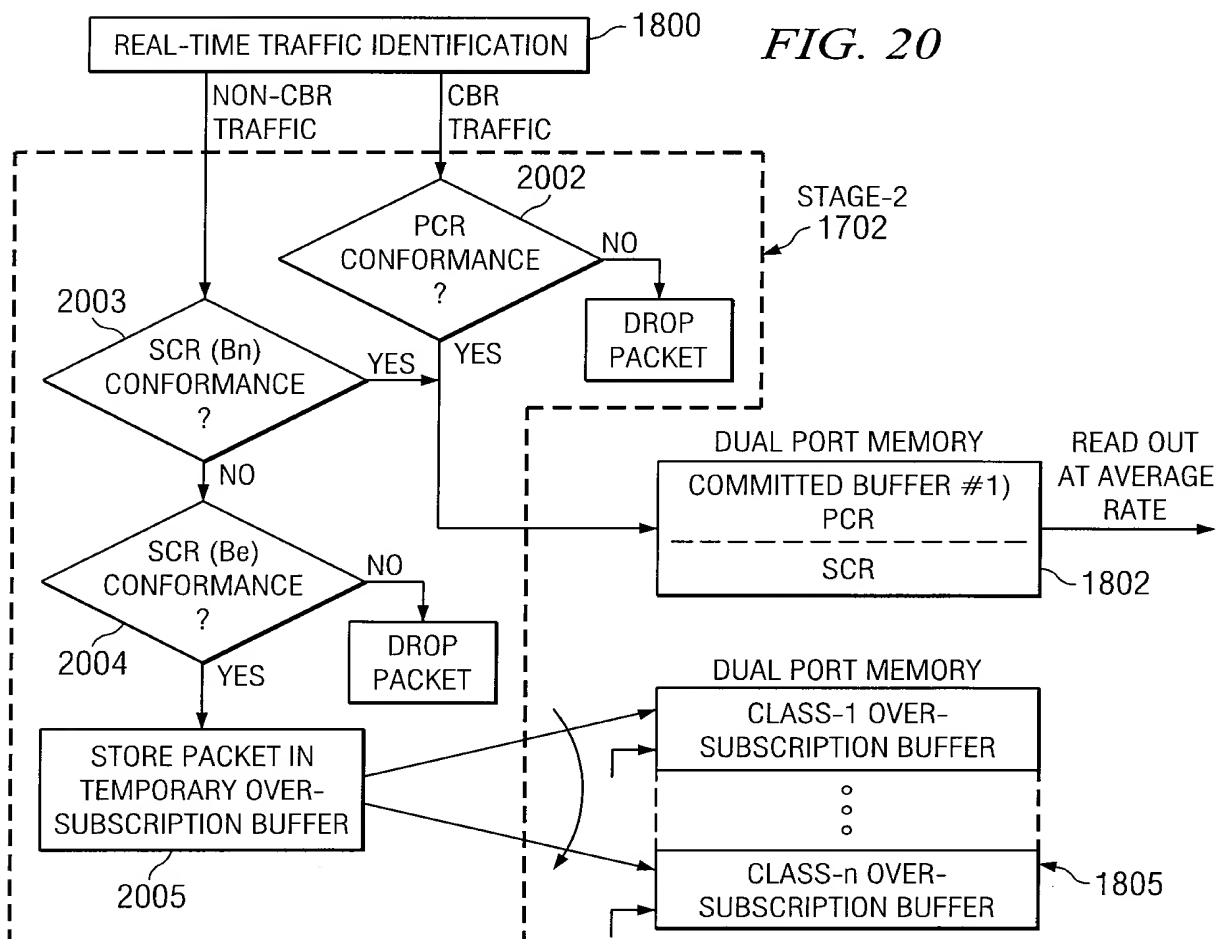
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TRAFFIC
IDENTIFICATION TABLE

1901

FIG. 19

| | |
|------------------|--|
| ETHERNET PORT #2 | BASED ON PER PORT CONTRACT VBR- nrt TRAFFIC, MEAN RATE 2 Mb/s NORMAL BURST SIZE 500 BYTES, EXCESS BURST SIZE 1500 BYTES |
| ETHERNET PORT #4 | BASED ON PER DSCP CONTRACT TOSxxx : VBR- nrt, MEAN RATE 4 Mb/s NORMAL BURST SIZE 128 BYTES EXCESS BURST SIZE 500 BYTES TOSyyy : PCR, MEAN RATE 64 Kb/s DELAY VARIATION 50 μ s |
| | |



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FIG. 21

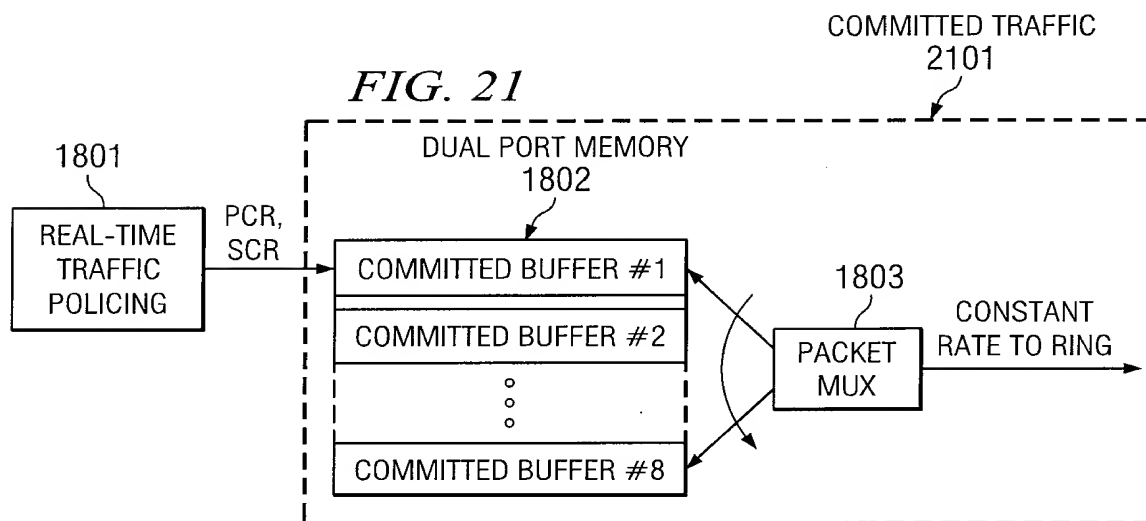
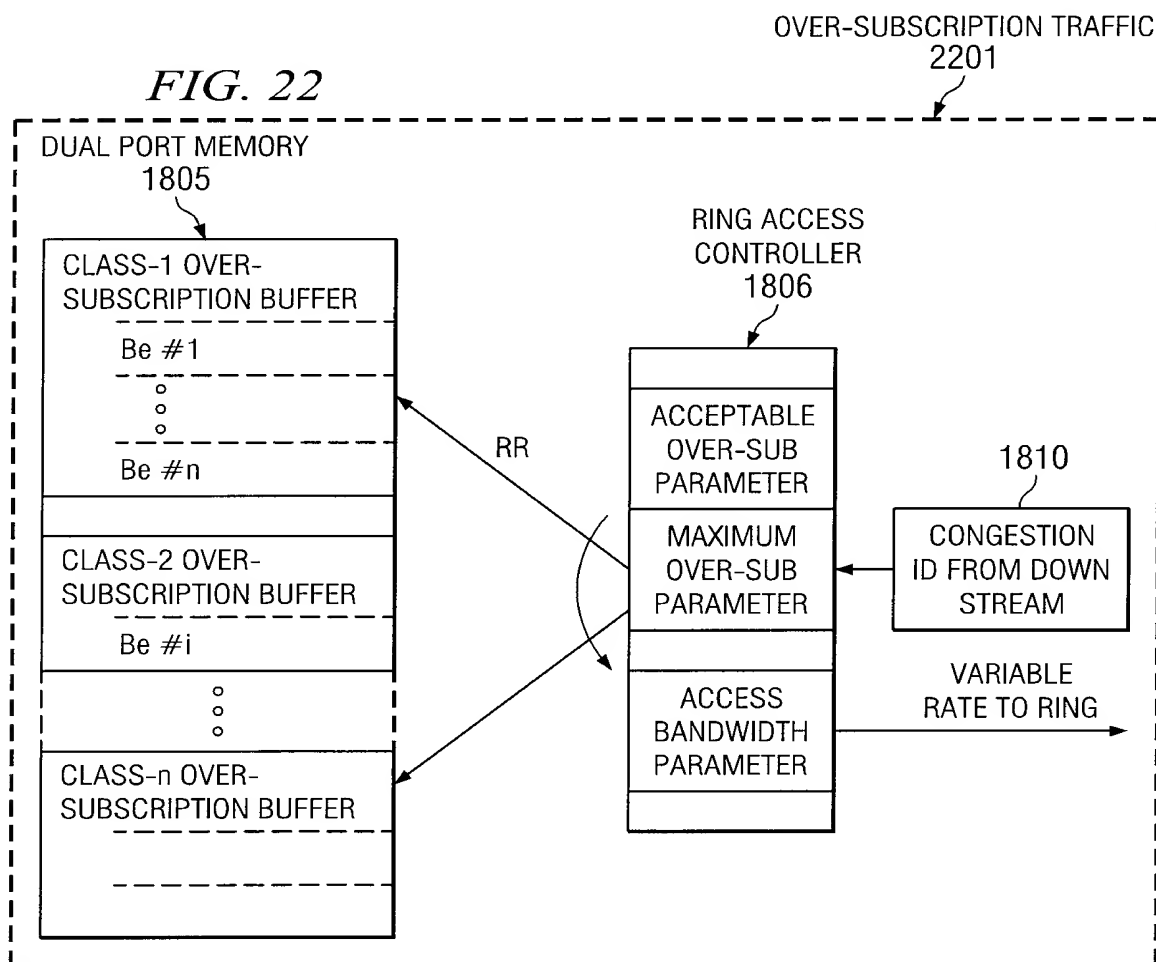


FIG. 22



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DUAL PORT MEMORY
1805

RING ACCESS
CONTROLLER
1806

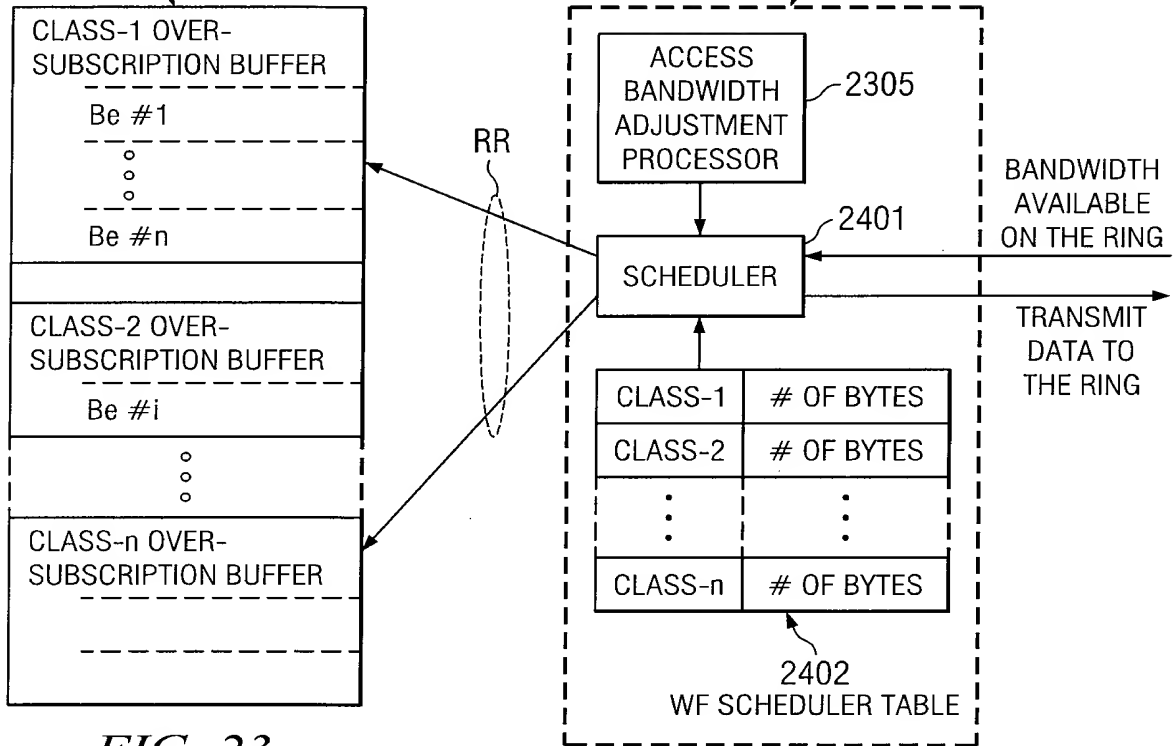


FIG. 23

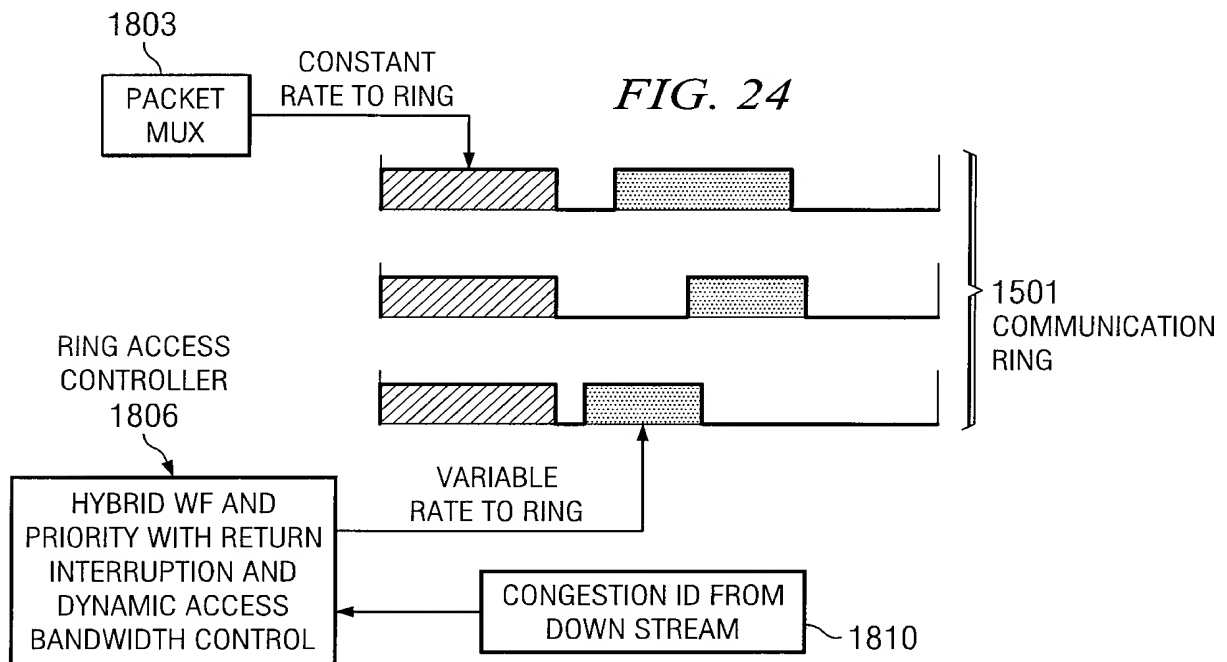


FIG. 24